**ECE 4723 Lab 5 Schematic Errata**

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The RTS# pin (pin 6) of the FTDI header requires a jumper to the MCLR pin (pin 1) of the ICSP header. This is required as the RTS# pin is not connected and is used for resetting the board on programming.

The RXD pin (pin 5) of the FTDI header requires a jumper to the RF0 pin located in the H1 header. This is required as the pin RXD is connected to by default is a input pin instead of an output pin.

The voltage divider connected to the LCD is missing a connection between R6, R7 and pin 3 of the LCD module. This required soldering a jumper between R6 and R7.